

Notice of References CitedApplication/Control No.
09/976,927Applicant(s)/Patent Under
Reexamination
RAMANATH ET AL.Examiner
Erik KielinArt Unit
2813

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, 2nd ed., Lattice Press: Sunset Beach CA, 2000, pp. 438, 782-783.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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